

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Desmicht et al.

Group Art Unit: 2432

Serial No.: 10/536,732

Examiner: Okeke, Izunna

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For: CHIP INTEGRATED PROTECTION MEANS

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO FINAL OFFICE ACTION

Sir:

In response to the Final Office Action notified on August 5, 2009, please find the following:

Remarks/Arguments begin on page 2 of this paper.